PTO/SB/08B (08-00) Approved 1-7 se through 10/31/2002. OMB 0651-0031

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Sheet 1 of 1

| Complete if Known | | |
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| Application Number | 09/372,879 | |
| Filing Date | Aug. 12, 1999 | |
| First Named Inventor | Stefanos Sidiropoulos | |
| Group Art Unit | 2814 | |
| Examiner Name | Farahani, Dana | |
| Attorney Docket Number | RAMB-01014US0 | |

| | | OTHER RELOG: ART - NON PATENT LITERATURE DOCUMENTS | |
|----------------------|--------------|---|-----|
| Examiner Initials | Cite No.1 | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T2 |
| | | "A Partial Design Analysis of The Samsung KM416H4031T-G10 4Mx16 Double Data Rate | |
| | | SDRAM", Semiconductor Insights Inc., March 1999 | |
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